Attorney Docket No.

018656-232



## UNITED STATES PATENT AND TRADEMARK OFFICE

### **MAIL STOP AF**

In re Patent Application of

Kazunori Shionoya

Application No.: 09/824,836

Filing Date:

Sir:

April 4, 2001

Title: MEMORY BOARD

Group Art Unit: 2622

Examiner: MARK R MILIA

Confirmation No.: 1177

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### AMENDMENT/REPLY TRANSMITTAL LETTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Enc	losed is a reply for the above-identified patent application.						
	A Petition for Extension of Time is also enclosed.						
	Terminal Disclaimer(s) and the \$65.00 (2814) \$130.00 (1814) fee per Disclaimer due under 37 C.F.R. § 1.20(d) are also enclosed.						
	Also enclosed is/are						
	Small entity status is hereby claimed.						
	Applicant(s) requests continued examination under 37 C.F.R. § 1.114 and enclose the   ☐ \$395.00 (2801) ☐ \$790.00 (1801) fee due under 37 C.F.R. § 1.17(e).						
	Applicant(s) requests that any previously unentered after final amendments <u>not</u> be entered. Continued examination is requested based on the enclosed documents identified above.						
	Applicant(s) previously submitted						
	on, for which continued examination is requested.						
	Applicant(s) requests suspension of action by the Office until at least, which does not exceed three months from the filing of this RCE, in accordance with 37 C.F.R. § 1.103(c). The required fee under 37 C.F.R. § 1.17(i) is enclosed.						
	A Request for Entry and Consideration of Submission under 37 C.F.R. § 1.129(a) (1809/2809) is also enclosed.						

# **Buchanan Ingersoll PC**

Attorney Docket No. 018656-232
Application No. 09/824,836

No additional claim fee is required.

An additional claim fee is required, a	and is calculated as shown below.
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		AMI	ENDE	ED CLAIMS				
	No. of Claims	Highest of Clain Previous Paid Fo	ns sly	Extra Claims		Ra	te	Additional Fee
Total Claims		MINUS	=	0	x	\$50.00	(1202) =	\$ 0.00
Independent Claims		MINUS	=	0	x	\$200.00	0 (1201) =	\$ 0.00
If Amendment adds n	nultiple depen	dent claims,	add \$	360.00 (1203)				
Total Claim Amendment Fee						\$ 0.00		
☐ Small Entity Status claimed - subtract 50% of Total Claim Amendment Fee					\$ 0.00			
TOTAL ADDITIONAL CLAIM FEE DUE FOR THIS AMENDMENT					\$ 0.00			

Ш	A check in the amount of	of i	is enclosed for the fee due
	Charge	to Deposit Accour	nt No. 02-4800.
	Charge	to credit card. Fo	orm PTO-2038 is attached.

The Director is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17, 1.20(d) and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

**BUCHANAN INGERSOLL PC** 

P.O. Box 1404 Alexandria, Virginia 22313-1404 (703) 836-6620

Date: September 28, 2005

William C. Rowland Registration No. 30,888



## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of	MAIL STOP AF			
Kazunori Shionoya	Group Art Unit: 2622			
Application No.: 09/824,836	Examiner: MARK R MILIA			
Filed: April 4, 2001	) Confirmation No.: 1177			
For: MEMORY BOARD	) ) )			

#### **RESPONSE AFTER FINAL**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 30, 2005, the Examiner is respectfully requested to reconsider the application and withdraw the outstanding rejections.

Claims 1-9 have been rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,535,368, hereinafter *Ho et al.* As argued in the response filed on February 16, 2005, the substance of which is incorporated herein by reference, *Ho et al.* does not disclose a memory board including a memory controller as well as a memory device. In response to the remarks filed on February 16, 2005, the Examiner indicated that "the Examiner understands" that the memory subsystem 10 illustrated in Figure 1 of *Ho et al.* is a "memory board" and as such the memory modules 12A and 12B can be connected to add additional memory capacity to the system. However, other than reciting the Examiner's understanding, the Examiner has provided no basis for the position that the memory subsystem 10 is a "printed wiring board" as recited in claims 1 and 7 or a "memory board" as recited in claim 4.